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<input type="checkbox"/>	L14	L13 and I3	2
<input type="checkbox"/>	L13	L12 and (network or server)	68
<input type="checkbox"/>	L12	L9	82
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L11	L10 and poll\$	1
<input type="checkbox"/>	L10	L8	181
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L9	L8 and (poll or polling)	82
<input type="checkbox"/>	L8	(PCB or (printed near board)) same (firmware or microcode or operating system or programmable)	1084
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<input type="checkbox"/>	L7	L5 and (PCB or (printed near board))	3
<input type="checkbox"/>	L6	L5 and poll\$	3
<input type="checkbox"/>	L5	L4 or 20030005207 or 20030120974	41
		(4914576 or 5101490 or 5343478 or 5425036 or 5428800 or 5465056 or 5761462 or 5802268 or 5826048 or 5864486 or 5894571 or 5933614 or 5970005 or 6031391 or 6044025 or 6137738 or 6167477 or 6198303 or 6255849 or 6289406 or 6314550 or 6366973 or 6405276 or 6427198 or 6430710 or 6459297 or 6460108 or 6501682 or 6526332 or 6529989 or 6551225 or 6567414 or 6614259 or 6622206 or 6629179 or 6636927 or 6658508 or 6658519 or 6691205).pn.	
<input type="checkbox"/>	L4		39
<input type="checkbox"/>	L3	L2 or 716/15.ccls.	6101
<input type="checkbox"/>	L2	L1 or 702/117-120.ccls. or 710/36-47,109,220.ccls.	5936
<input type="checkbox"/>	L1	717/124-127,168-178.ccls.	2874

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OC3MON: flexible, affordable, high performance statistics.. - Joel Apisdorf (Correct)  
cards in the original OC3MON design required **polling** the interface card for cells at 1/128 the cell  
us to optimize OC3MON operation with custom **firmware**. We made arrangements with Fore to obtain the  
difficult, and yet more critical, to characterize **network** usage and workload trends, and point to the  
[www.vbns.net/presentations/papers/INET97/inet97.ps.gz](http://www.vbns.net/presentations/papers/INET97/inet97.ps.gz)

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**1 [U-Net: a user-level network interface for parallel and distributed computing \(includes URL\)](#)**

T. von Eicken, A. Basu, V. Buch, W. Vogels

December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5

Full text available: pdf(1.84 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**2 [Process exchange on the PR1ME family of computers](#)**

Edward A. Feustel

March 1984 **ACM SIGARCH Computer Architecture News**, Volume 12 Issue 1

Full text available: pdf(751.77 KB)

Additional Information: [full citation](#), [abstract](#), [references](#)

A high speed mechanism for process exchange is essential in a time sharing system based on the use of many processes. The automated process exchange mechanism on the Model P400 and the 50 Series of PR1ME machines is described. Typical timing for operations using the process exchange mechanism on the P750 is given. Its use in PRIMOS is explained and validated.

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PCB polling network firmware

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### Hardware for Better Firmware

... being so intimate with both the hardware and the **firmware**, could, and did,  
... If **PCB** real estate is so limited that there's no room for the level ...

[www.ganssle.com/articles/Hardwareforbetfirm.htm](http://www.ganssle.com/articles/Hardwareforbetfirm.htm) - 22k - [Cached](#) - [Similar pages](#)

### [PDF] BDS Firmware Release Notes 2.36

File Format: PDF/Adobe Acrobat

... The **firmware** upgrade protocol now supports old and new protocols, so now it  
... **PCB** must be revision B or. later. - Now supports internal **network** cards. ...

[www.alber.com/Docs/BDSRelNotesR2360205Web.pdf](http://www.alber.com/Docs/BDSRelNotesR2360205Web.pdf) - [Similar pages](#)

### ESXMIM Firmware Version 2.10.06

... The DLM option allows the ESXMIM to locally **poll** SNMP and IP capable devices  
... **firmware** image through any **network** port, including the BRIM interface. ...

[www.enterasys.com/support/relnotes/rn\\_1267-06.html](http://www.enterasys.com/support/relnotes/rn_1267-06.html) - 45k - [Cached](#) - [Similar pages](#)

### [PDF] DeviceNet in 90 Days or Less: A Plan for Product Developers

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... often outperform a **polling network** operating at several times the speed. ...  
An Add-on **PCB** plugs into your device internally (usually TTL-level ...

[www.rtaautomation.com/devicenet/494ds/DeviceNet\\_in90Days.pdf](http://www.rtaautomation.com/devicenet/494ds/DeviceNet_in90Days.pdf) - [Similar pages](#)

### Resume: Joe Drown, Senior Principle Software Engineer

... Real time embedded **firmware** development in C/C++/ASM utilizing custom and ...  
CAE tool software development for schematic capture and **PCB** layout. ...

[www.scgild.com/Resume/5912R.html](http://www.scgild.com/Resume/5912R.html) - 7k - [Cached](#) - [Similar pages](#)

### BIOSMAN BIOS chips bios upgrades update bios replacement bios ...

... The primary **PCB** in a system is called a system board or motherboard, ...  
When many stations are connected to the same circuit, **polling** from the center ...

[www.biosman.com/glossary\\_P.htm](http://www.biosman.com/glossary_P.htm) - 73k - [Cached](#) - [Similar pages](#)

### Embedded Computing Design - USB Trends: Embedded Systems, USB On ...

... An embedded system is defined as hardware and **firmware** that usually has an  
... scheduling of the **polling** of USB devices, and memory management. ...

[www.embedded-computing.com/articles/wright\\_et\\_al/](http://www.embedded-computing.com/articles/wright_et_al/) - 34k - [Cached](#) - [Similar pages](#)

### ODVA - The Open DeviceNet's Vendor Association

... to a fully DeviceNet-enabled controller with the RTA Valve Control **PCB**. ...  
We had RTA make a product **firmware** change in the middle of a 200 piece ...

[www.odva.org/10\\_2/04\\_products/814-1RT01.htm](http://www.odva.org/10_2/04_products/814-1RT01.htm) - 14k - [Cached](#) - [Similar pages](#)

### [PS] Lazy Receiver Processing (LRP): A Network Subsystem Architecture ...

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... Assuch, it can be readily integrated in a **network** interface's **firmware**, ...  
With the exception of demultiplexing code (early demux in LRP versus **PCB** ...

[www.cs.rice.edu/CS/Systems/LRP/osdi96.ps](http://www.cs.rice.edu/CS/Systems/LRP/osdi96.ps) - [Similar pages](#)

embedded.com/showArticle.jhtml?articleID=51202213 - 100k - Cached - Similar pages

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 machine utilizing CAN **network** with embedded Pentium ... **PCB** layout of a PC based Microchip PIC microcont  
 rd interrupt driven ...  
[www.scguild.com/Resume/5205R.html](http://www.scguild.com/Resume/5205R.html) - 6k - [Cached](#) - [More from this site](#)


[LN6.22 - Release Notes Fw. v6.22 Zyxel U-1496](#)   
 yXEL Communications Services GmbH. ... NMS (**Network** Management System) RM622SN.U24 ..... U1496SI  
 ceiving & transmission procedure ... only effect on **PCB** REV6, REV8 or ...  
[www.zyxteltech.de/rln\\_u-1496.htm](http://www.zyxteltech.de/rln_u-1496.htm) - 96k - [Cached](#) - [More from this site](#)


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 - 1 - JTL **FIRMWARE** VARIATIONS ... to reduce inter-unit **polling** delays. **Network** communications performan  
 formation added on items ...  
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 EE TIMES **NETWORK**. Online Editions ... are creating an operating system, **firmware**, or even device drivers, t  
 op represents a ...  
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[NPM06](#)   
 PMUX **firmware**, form a ... 12005 (**PCB** build option ... **polling** and one to be polled by master. · communication  
**etwork** ...  
[www.security2k.com/manuals/12005 NPM06\(digimux\).htm](http://www.security2k.com/manuals/12005 NPM06(digimux).htm) - 410k - [Cached](#) - [More from this site](#)

[resh Patents-Smart camera patent apps](#)   
 circuit board (**PCB**). Once all parts ... **network** address to each control module 112. The host computer 116 the  
**olling** is required ...  
[www.freshpatents.com/Smart-camera-dt20050127ptan20050021157.php](http://www.freshpatents.com/Smart-camera-dt20050127ptan20050021157.php) - 45k - [Cached](#) - [More from this site](#)

<http://de.lunar-linux.org/lunar/patches/ipw2100-2.6.3-0.30-patch>   
 n other laptops, however, the switch +is controlled through a button being pressed and a software driver then me  
 age, entering ... ipw2100 **firmware**=/usr/share/firmware/ipw2100-1.0 ... **firmware**. You are +now ready to load  
 /etc/sysconfig/**network** ...  
[de.lunar-linux.org/lunar/patches/ipw2100-2.6.3-0.30-patch](http://de.lunar-linux.org/lunar/patches/ipw2100-2.6.3-0.30-patch) - 259k - [Cached](#) - [More from this site](#)

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(9744) Seeking SRM Firmware ... Network Datalink Errors and Telnet? (9663) DECnet NetRAIN-like Capabilities  
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 THE M50FW040 FIRMWARE HUB. Related Information. PC/Server BIOS Flash ... the power supply. The PCB t  
 suing the Program/Erase ...  
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 PCB layout of a PC based Microchip PIC microcontroller programmer. \* Coded firmware for an elapsed time in  
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 1M x8, Uniform Block) 3V Supply Firmware Hub Flash Memory ... the power supply. The PCB track widths mus  
 e Program/Erase ...  
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[packages.debian.org/testing/allpackages](http://packages.debian.org/testing/allpackages) - 1113k - [Cached](#) - [More from this site](#)

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 ecifications and ... FQH TRAINING GUIDE SERVER TERMINAL GUIDE CONDENSED REFERENCE ...  
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 P3 Player. If you're not familiar with MP3's, here's a good place to start. The release of the MAS3507D chip, from  
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 VB MP3 ...  
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